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Cmos Current Comparator With Regenerative

Current mode CMOS multivalued logic circuits are interesting and have many applications in wireless communications. This paper shows the CMOS multi valued current comparator design and to obtain precise output using regenerative property.

CMOS current

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property - CORE**

Analog Integrated
Circuit Design 6. CMOS
Comparators 1
Performance
characteristics A
comparator detects if
its input (voltage or
current) is higher or
lower than a reference
level. Its output is a
large voltage which is
assumed to represent
a digital 1 or 0 level.

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6. CMOS

Comparators - IMS

High-performance CMOS current comparator X. Tang and K.-P. Pun A new high-performance CMOS current comparator is proposed. By adding two inverters in the feedback loop of Traff's comparator, the proposed comparator exhibits significant speed improvement especially for low input

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currents. Simulated in
a 0.18 μm CMOS tech-

Property
**High-performance
CMOS current
comparator**

□ Comparator = □
Preamp (optional) □ +
Reference Subtraction
(optional for single-bit
case) □ + Regenerative
Latch □ + Static Latch
to hold outputs
(optional)

**CMOS Comparator
Design -**

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lumerink.com

CMOS COMPARATOR

Reg. Speed -Linear
Model ... Ref: B.-S.

Song et al., "A 1 V 6 b
50 MHz current-
interpolating CMOS
ADC," in IEEE Symp.

VLSI Circuits, 1999, pp.
79-80. M 1 M 2 M 5 M 3

M 4 $V_i + V_i - V_o + V_o -$
R L R L X. PRE-AMP

EXAMPLE ... • Large
Regenerative gain for
resolving small inputs •
Metastability (wrong or
incomplete decisions ...

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CMOS Regenerative COMPARATORS

The comparator is designed using differential input stages with regenerative S-R latch to achieve lower offset, lower power, higher speed and higher resolution. In order to decrease circuit complexity, a comparator should maintain power, speed, resolution and offset-

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voltage properly.

Regenerative
Property
**Design of High
Speed and Low
Offset Dynamic
Latch ...**

CMOS Comparators
Basic Concepts Need to
provide high gain, but
it doesn't have to be
linear $\frac{3}{4}$ Don't need
negative feedback and
hence don't have to
worry about phase
margin. $\frac{3}{4}$ The gain
can be obtained in
multiple stages.

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Important parameters:
Offset (and noise),
speed, power
dissipation, input
capacitance, kickback
noise, input CM range.

CMOS Comparators

The comparator is designed in a 0.35 μm CMOS process with a supply voltage of 3.3 V. The comparator is designed for time-interleaved bandpass sigma-delta ADC. Due to the nature of the

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target application, it should be possible to turn off the components to avoid the static power consumption.

DESIGN OF A HIGH-SPEED CMOS COMPARATOR

REGENERATIVE COMPARATOR (Schmitt trigger) Schmitttrigger is a comparator circuit with positive feedback, as gain can be increased

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greatly. The major application of this comparator is to overcome the noise voltages which reduced the problem with zero crossing detector circuit. It converts the analog signal to digital signal.

REGENERATIVE COMPARATOR (Schmitt trigger) - Classes/Loosers

Abstract: The low voltage clocked

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comparator provides maximum speed and power efficiency and is thus required for implementing area efficient and ultra low-power analogue to digital converters (ADCs). For an analog and mixed signal design, comparator is the main component in low-power applications.

Low-power and high performance clocked

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regenerative ...

CMOS Comparator

Example Ref: A.

Yukawa, "A CMOS 8-Bit
High-Speed A/D

Converter IC," JSSC

June 1985, pp. 775-9

- Flash ADC: 8bits,
+/-1/2LSB INL @

fs=15MHz (Vref=3.8V,
LSB~15mV)

**Latched Comparator
- University of
California, Berkeley**

CMOS comparators

with and without

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hysteresis. Different types of comparators are discussed, mainly the three-stage comparator and folded-cascode comparator. It also discusses the advantages of comparators with programmable hysteresis.

Optimizations are done in order to obtain minimum DC offsets. The input pulse frequency is 100 kHz.

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**Low-Voltage CMOS
Comparators With
Programmable
Hysteresis**

In the literature, various kinds of CMOS comparators can be found. The types of the comparators can be classified largely into three: Open-loop Comparators (op-amps without compensation) , Pre-amplifier Based Latched Comparators (open-loop comparator combined with

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dynamic regenerative
latch), and Fully
Dynamic Latched
Comparator. In this
paper,

**Low-power high-
speed low-offset
fully dynamic CMOS
latched ...**

The current
comparator consists of
a current amplifier
(M1M4 and Rp), a Class
B output stage
(M7/M8), and three
CMOS inverters

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Comparator With
Regenerative
Property
(M5/M6, M9-M12). The proposed design is a modified version of the simple current comparator in, with an added current amplifier and an extra pair of inverters compared to the original design.

cmos -

DocShare.tips

"Designing

Regenerative

Comparators in CMOS

for Low Offset and

Noise" Subthreshold or

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analog sub-1V power supplies operation, are getting famous for their energy efficiency due to voltage scaling effect. The lecture discusses how to balance specifications on noise and offset with comparator delay and bit-error-rate in communications receivers.

Designing Regenerative Comparators in

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... Regenerative
Property

how to design
regenerative latch Hi,
I've just read
Gregorians book
"CMOS-Opamps and
Comparators". In
chapter 8.3 he
introduces to a sample
design of a
regenerative latch
based comparator with
hysteresis. On the last
page of the chapter (p.
354) he adds that one
might optimize the

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Regenerative Property Transient response of comparator (regenerative latch

...

There is a bypass switch 60coupled between the first and second inputs 10, 20which provides a current path for the input signal current when the inputs 10, 20are decoupled from the regenerative...

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US7106106B2 -

**Clocked comparator
circuit - Google
Patents**

Depends on current
charging/discharging a
capacitor Slew Rate =

... Higher Gain

Comparators M8-M11

form CMOS Inverters

Comparator Circuits

2-stage Comparator

Does not require

compensation. Why?

Performance Metrics

Maximum Output

Voltage Minimum

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Comparator With
Output Voltage ...

Regenerative
Comparators High
Speed Comparators Pre-
amp (High GB) + Latch
+ Output ...

What are Comparators?

Input sampling, offset
correction and
common-mode
cancellation have been
incorporated into a
pipelined cascade of
regenerative sense
amplifiers. Integrated

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in a standard 2- μm CMOS technology, the comparator operates at a maximum sampling rate of over 100 MHz, while dissipating only 3.6 mW of power from a single +5-V supply.

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ecf8427e.